




AF
2800
#818

Serial No.: 09/523,990 RECEIVED Art Unit 2876

OCT 22 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an Envelope Addressed to The Commissioner of Patents & Trademarks, Washington, D.C. 20231
15 October 2002
Date of Deposit
Stephen B. Ackerman, Reg. No. 37,761  15 October 2002
Name of Person Making Deposit Signature Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Do NOT ENTER
D.W.
10-17-02

Inventors:	Mou-Shiung Lin et al.	Date :	15 October 2002
Serial No.:	09/523,990	Art Unit:	2876
Filing Date:	13 March 2000	Examiner:	Daniel J. Walsh
Title:	Method of Manufacture and Identification of Semiconductor Chip Marked for Identification with Internal Marking Indicia and Protection Thereof by Non-Black Layer and Device Produced Thereby	Attorney:	George O. Saile 28 Davis Avenue Poughkeepsie, New York, 12603

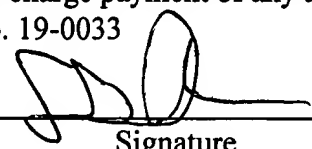
AMENDMENT AFTER FINAL

entered
2/20/03
ann

Box AF
Assistant Commissioner for Patents
Washington, D. C. 20231

Your Honor:

In response to the Office Action of 14 August 2002, please amend the above-identified application as follows:

The Commissioner is hereby authorized to charge payment of any additional fees involved with claims and the like to Deposit Account No. 19-0033
Stephen B. Ackerman, Reg. No. 37,761  15 October 2002
Name Signature Date